

AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application.

Listing of Claims:

Claim 1 (Currently amended) A receiver for a CDMA system, intended to receive signals corresponding to spread spectrum information symbol streams ~~in~~ by pseudorandom binary sequences, ~~this said receiver including K comprising~~ processing channels (V_1, V_2, \dots, V_K) ~~being characterised in that wherein:~~

at least one of ~~these said~~ channels (V_1) includes

a filter (20) adapted to one of ~~the said~~ pseudorandom binary sequences having been used for information symbols spectrum spreading, and

a ~~symbol clock signal (Hs) recovery circuit (12) configured to produce a clock signal,~~

~~the other channels (V_2, \dots, V_K) each including~~ includes a sliding correlator (~~30₂, ..., 30_K~~) working with ~~one of the other~~ another one of said pseudorandom binary sequences having been used for information symbols spectrum spreading, each sliding correlator being controlled by ~~a symbol clock signal, which is the said~~ clock signal (H_s) produced by ~~the said~~ recovery circuit of said at least one channel (V_1) using the which includes said adapted filter (20).

Claim 2 (New) The receiver according to Claim 1, wherein said adapted filter is a digital filter with coefficients adapted to said one of said pseudorandom binary sequences.

Claim 3 (New) The receiver according to Claim 1, wherein said adapted filter is configured to process said signals corresponding to spread spectrum information symbol streams.

Claim 4 (New) The receiver according to Claim 1, wherein said adapted filter is configured to maintain said clock signal controlling said sliding correlators.

Claim 5 (New) The receiver according to Claim 4, wherein said adapted filter is configured to maintain said clock signal permanently.

Claim 6 (New) The receiver according to Claim 1, wherein each of said channels further comprises a processing circuit.

Claim 7 (New) The receiver according to Claim 6, wherein said processing circuit is configured to perform delayed multiplication.

Claim 8 (New) The receiver according to Claim 6, wherein said processing circuit is configured to perform a channel estimation.

Claim 9 (New) The receiver according to Claim 1, wherein each of said channels further comprises a decision circuit.

Claim 10 (New) The receiver according to Claim 1, comprising a plurality of channels, each including a filter adapted to one of said pseudorandom binary sequences.

Claim 11 (New) The receiver according to Claim 10, wherein said plurality of channel produces a plurality of clock signals offset relative to each other.

Claim 12 (New) A receiver for receiving signals comprising:
a first channel configured to process a first signal of said signals and to recover a clock signal from said first signal;
a second channel configured to process a second signal of said signals, said second channel being controlled by said clock signal recovered by said first channel, said second channel being free of a clock signal recovering circuit.

Claim 13 (New) The receiver according to Claim 12, wherein said first channel maintains said clock signal.

Claim 14 (New) The receiver according to Claim 13, wherein said first channel maintains said clock signal permanently.

Claim 15 (New) The receiver according to Claim 12, wherein a spectrum of said signals is spread using sequences.

Claim 16 (New) The receiver according to Claim 15, wherein said sequences are at least one of pseudorandom or binary.

Claim 17 (New) The receiver according to any of Claims 15, wherein said first channel includes a filter adapted to a first sequence of said sequences.

Claim 18 (New) The receiver according to Claim 17, wherein said filter is a digital filter having coefficients adapted to said first sequence.

Claim 19 (New) The receiver according to Claim 12, wherein said second channel includes a sliding correlator.

Claim 20 (New) The receiver according to Claim 12, comprising a third channel configured to process a third signal of said signals and to recover a second clock signal from said third signal.

Claim 21 (New) The receiver according to Claim 20, wherein said clock signal recovered from said first signal and said second clock signal are offset relative to each other.

Claim 22 (New) The receiver according to Claim 12, wherein said signals correspond to CDMA signals.